

FIG. 1 (PRIOR ART)

Patented by Jianq Chyun IP Office

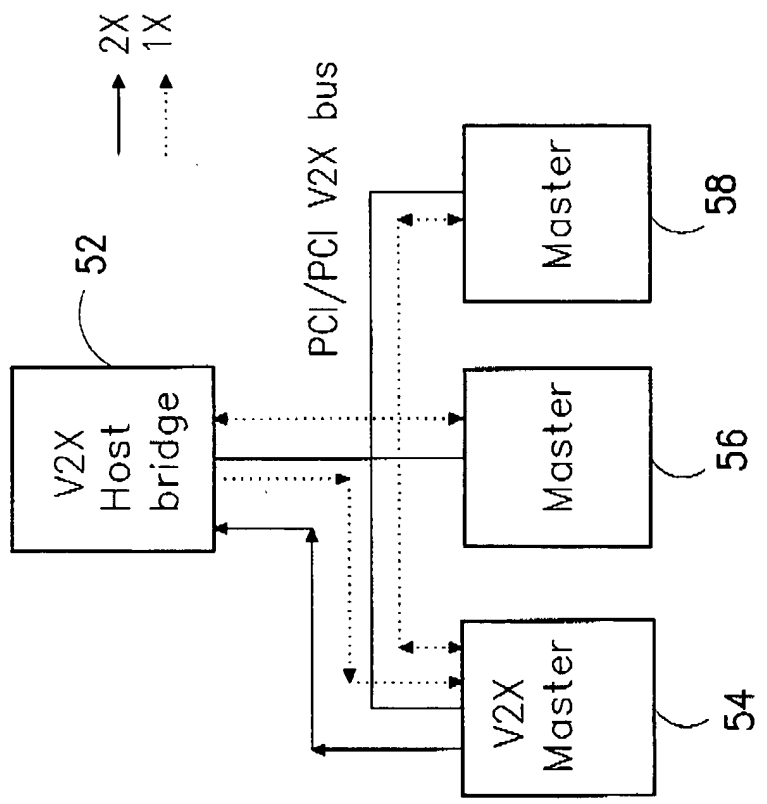


FIG. 2

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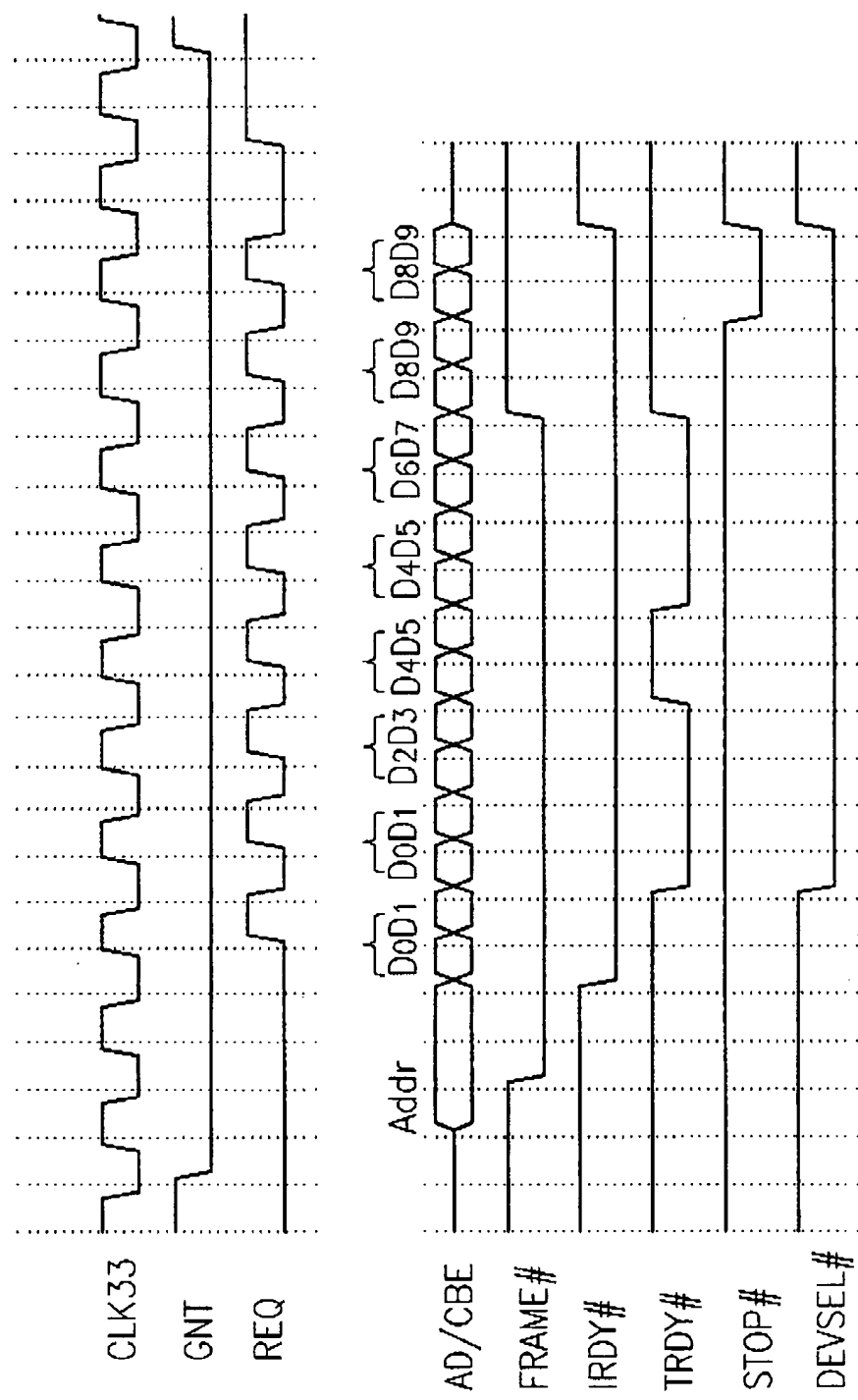
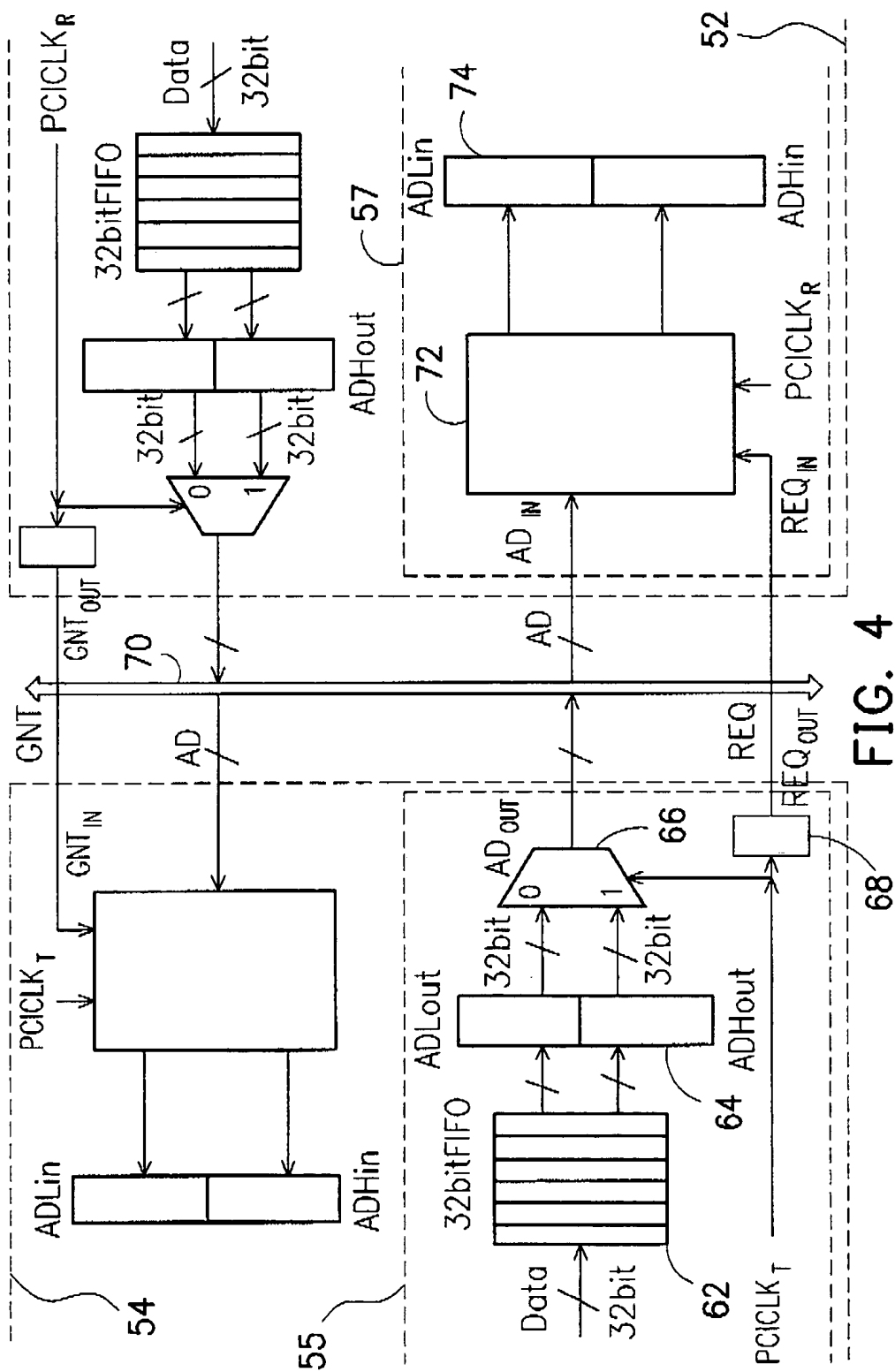


FIG. 3



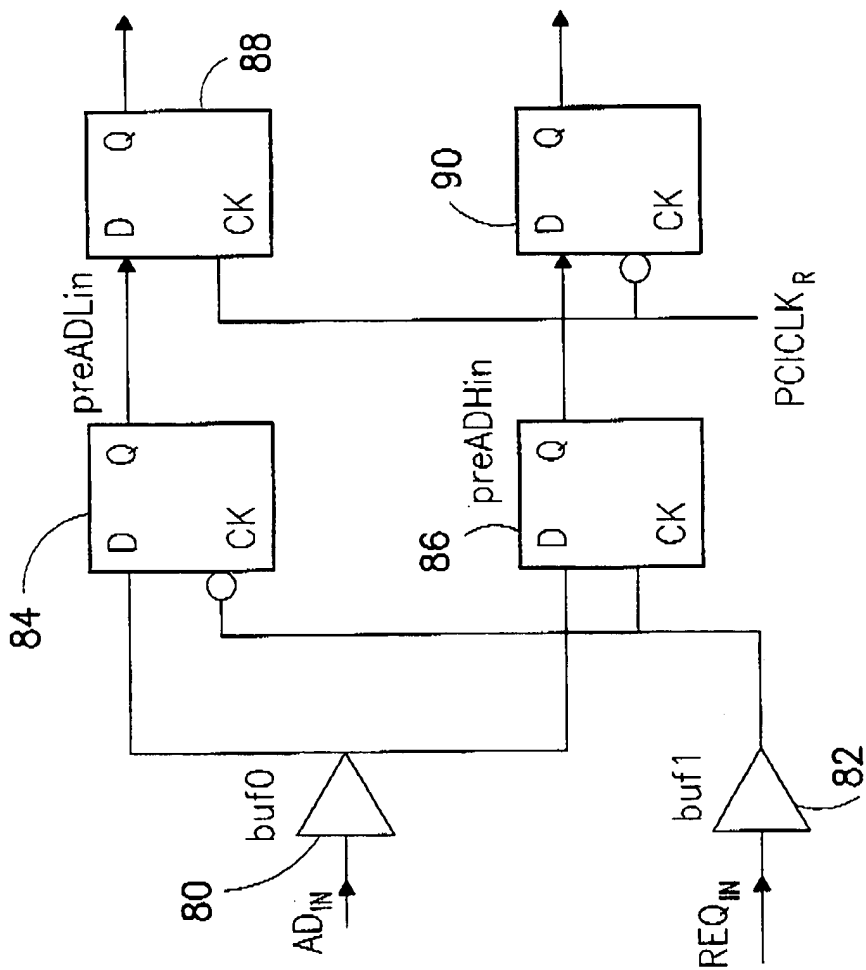


FIG. 5

FIG. 5 is a block diagram of the circuit of FIG. 1.

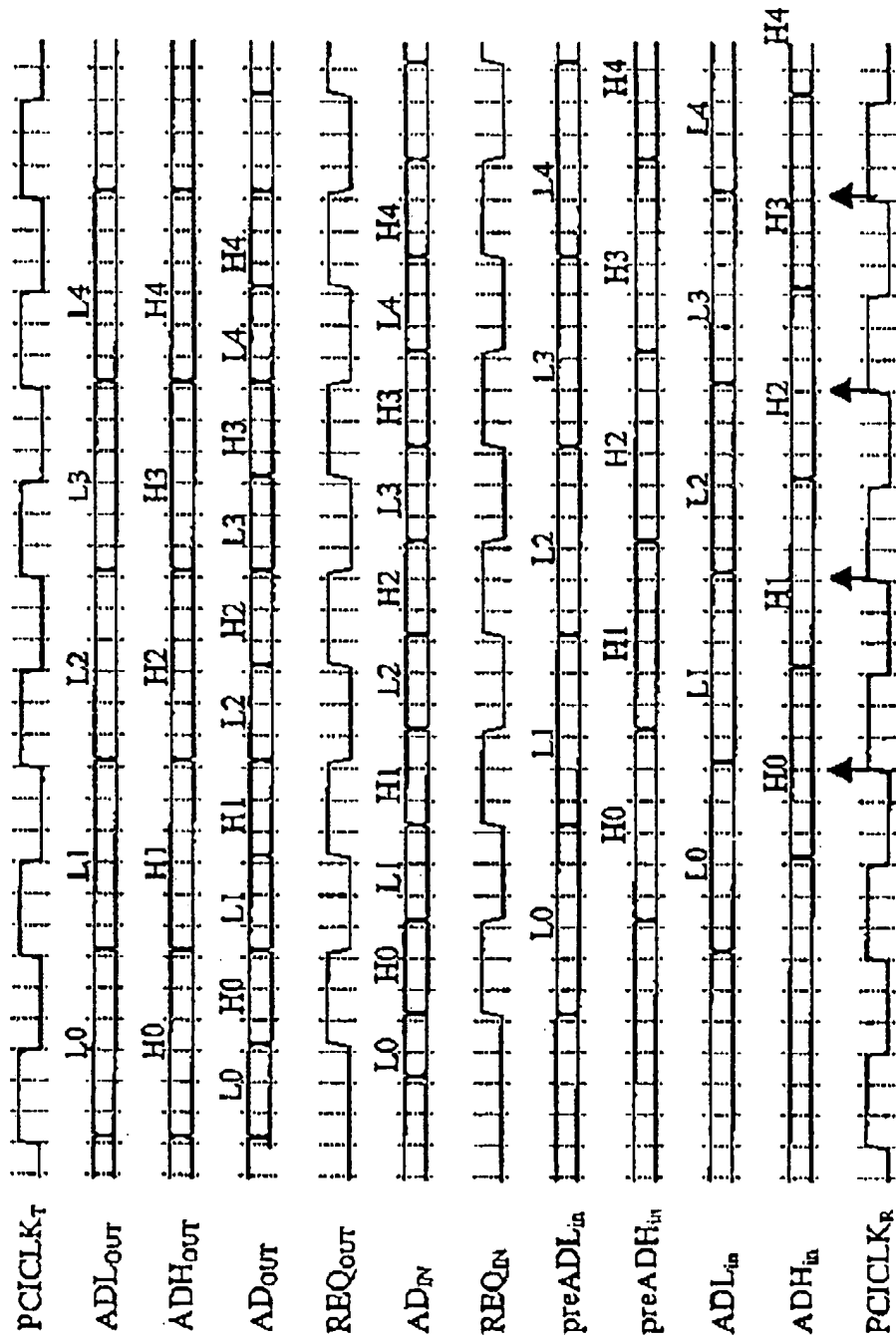


FIG. 6